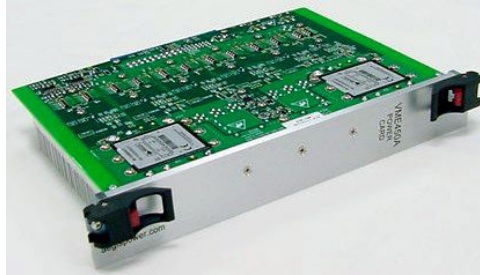


VME400A-001

VME DC-DC Power Converter Card

(Document Rev A05 11/03/2015)



**28Vdc Input
4 Output, 400W Max Combined**

Features

- 28Vdc per MIL-STD-704A-F * and MIL-STD-1275A/B/D * continuous operation.
- 4 Output Voltages, 400W max.
- MIL-STD-810F Environmental *
- MIL-STD-461E EMI *
- Dual Slot VME Power Card

* Designed to meet portions of the standard. Contact Aegis Power for details.

Table 1: Maximum Ratings

Parameter	Rating	Unit	Notes
Vin max range	18 to 36	Vdc	
Temperature	-40 to +85	°C	Baseplate temperature
Combined output power	400	W	
Input power	471	W	@ 400W out (28VDC input)
Max +5Vdc power	112	W	
Max +3.3Vdc power	112	W	
Max +12Vdc power	112	W	
Max -12Vdc power	112	W	

Product Highlights

This dual slot filtered 28Vdc 8HP wide x 6U high VME400A dc-dc power converter card with four outputs (3.3Vdc, 5Vdc, ±12Vdc) at 400W, is a military COTS solution designed to meet portions of MIL-STD-810F vibration and shock requirements and MIL-STD-461E EMI requirements. When compared to VME power supplies using conventional technology, the dual-slot VME400A-001 provides users with higher efficiency (86%), lower weight (4.14 pounds), and higher output power (up to 400W).

AEGIS Power Systems, Inc. specializes in the front end design, development, and manufacture of Rapid Response Custom Switching Power Supplies for defense, industrial, telecommunication, and electric powered vehicle applications. Contact Aegis Power Systems for details on Mil-Specs that this product is designed to meet.

SPECIFICATIONS

(Typical at 25°C, nominal line and 100% load, unless otherwise specified.)

DC input voltage:	Designed to meet MIL-STD-704A-F and MIL-STD-1275A/B/D, continuous operation, 22Vdc to 33Vdc, 28 Vdc nominal. 100Vdc 50 msec transient.
DC input line current:	21.41A Max @ 22VDC, 16.82A Typ @ 28VDC (400W out.)
Input power:	471W maximum @ 400W out.
Output power:	400W Maximum, all outputs combined.
Output voltages:	See table 2.
Efficiency:	86% minimum, 87% typical.
Start up time:	500 millisecond maximum.
Voltage set point/ Line/Load regulation:	+/- 2% Vout nominal (for any combination).
Temperature regulation:	+/- 0.01% / °C.
Output ripple:	50mV pk-pk Max. (20 MHz BW) all except; +/-12 Vdc 100mV pk-pk Max.
Current Limit:	Short circuit protected with automatic recovery.
Temperature:	-40°C to +85°C Operating baseplate wedgelocks 400W out. -55°C to +100°C Non-operating.
Cooling:	Customer provided forced air across power card cooling fins.
Package:	Pluggable dual slot slide in card with attached cooling fins.
Dimension:	6U x 8HP x 160mm (see mechanical drawing page 5).
Weight:	4.14 lb. Typical.
Connector:	1ea Positronics PCIH47M400A1 or equivalent (see pin assignments page 4).
Vibration:	Designed to meet MIL-STD-810F, Method 514.5, Procedure I.
Shock:	Designed to meet MIL-STD-810F, Method 516.5, Procedure I.
Humidity:	0 – 95% non-condensing.
EMI:	Designed to meet MIL-STD-461E (CE102 and CS101).
Options:	Designed to meet CE Mark (Low Voltage Directive 2006/95/EC).

Specifications subject to change without notice.

Table 2: Voltage Outputs

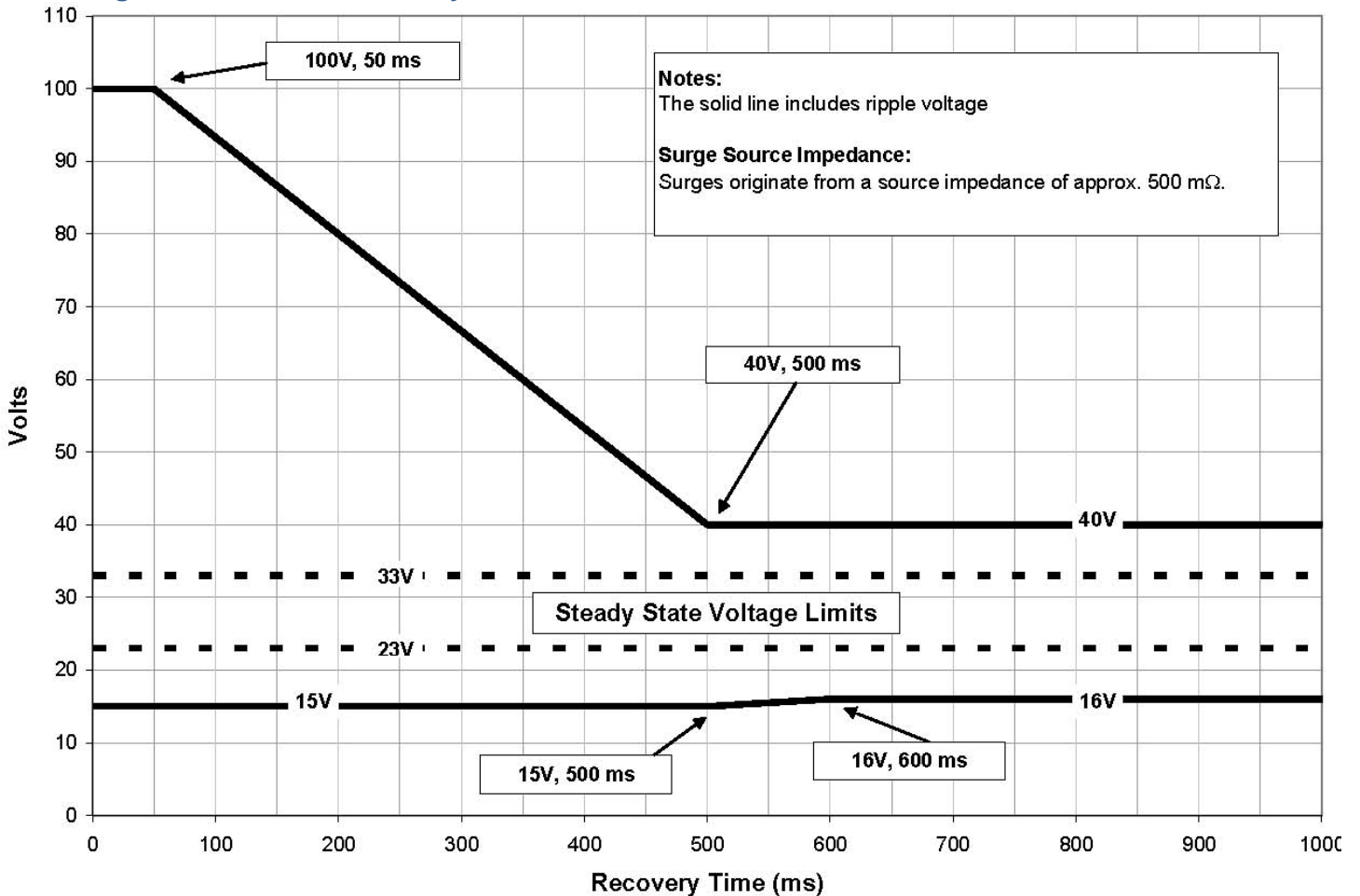
	V1	V2	V3	V4
VME400A-001-zzz	+5Vdc	+3.3Vdc	+12Vdc	-12Vdc
Maximum individual DC outputs	22.4A	33.9A	9.3A	9.3A
	112W	112W	112W	112W

* VME400A-001 Maximum total output power is 400W (all DC outputs combined).

Table 3: Customer selected options

-zzz	Custom Options
-000	No Options
-001	Conformal coating on PWB
-002	Metric wedgelocks
-003	Split inhibit control for V1/V3 and V2/V4
-004	Connector keyed (position #1)
-005	Connector keyed (position #2)
-006	Connector keyed (position #3)
-007	Conformal coating & Split inhibit for V4

Figure 1: Transient Immunity



Connector Pin Out Assignment

Positronic Connector P/N PCIH47M400A1 or Equivalent

Pins 1, 2, 3, 4 **+5 Vdc**
Pins 5, 6, 7, 8 **+5 V RTN (Common)**

Pins 9, 10, 11, 12 **+3.3 V RTN (Common)**
Pins 13, 14, 15, 16 **+3.3 Vdc**

Pin 17 **+12 Vdc**
Pin 18 **+12 V RTN (Common)**

Pin 19 **-12 Vdc**
Pin 20 **-12 V RTN (Common)**

Pin 21 NC

Pin 22 **POWER OK RTN (Common)**

Pins 23, 24, 25, 26 NC

Pins 27, 28, 29 NC

Pin 30 NC

Pin 31 NC

Pin 32 NC

Pins 33, 34, 35, 36 NC

Pins 37, 38 NC

Pin 39 **Inhibit (Connect pin to negative input to disable)**

Pins 40, 41 NC

Pin 42 **Power OK, (Open collector = Fail)**

Pins 43, 44 NC

Pin 45 **Chassis Ground**

Pin 46 **Positive Input**

Pin 47 **Negative Input**

Pin 48, 49 (Available for unique keying of supply)

** ALL PINS DESIGNATED NC SHOULD HAVE NO CONNECTION ON THE BACKPLANE.

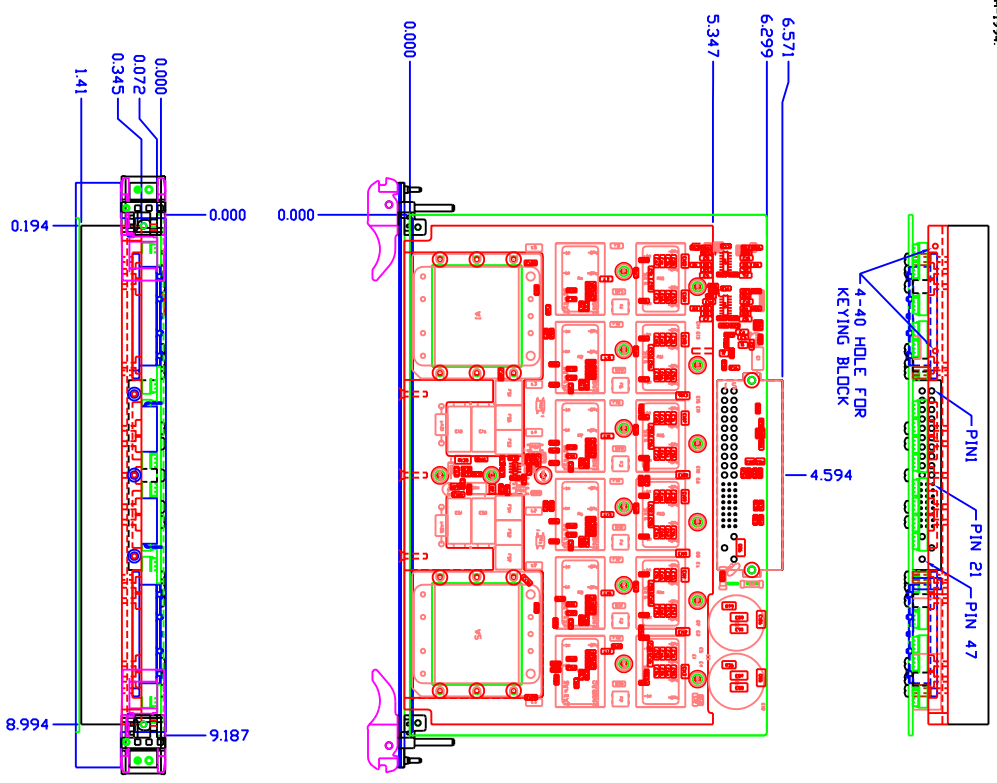
** ALL OUTPUT RTN PINS (COMMON) SHOULD BE TIED TOGETHER ON BACKPLANE.

** ALL PINS OF THE SAME VOLTAGE SHOULD BE TIED TOGETHER ON THE BACKPLANE (i.e. ALL 4 OF THE +5V PINS SHOULD BE TIED TOGETHER).

** TO DISABLE ALL OF THE DC OUTPUTS FROM THIS CARD-CONNECT PIN 39 (INHIBIT) TO PIN 47 (NEGATIVE INPUT), THIS CAN BE ACCOMPLISHED USING A FET, TRANSISTOR, RELAY OR SWITCH THAT CAN SINK AT LEAST 15ma.

** POWER OK SIGNAL IS AN OPEN-COLLECTOR TRANSISTOR OUTPUT. IT WILL BE LOW WHEN ALL OUTPUT VOLTAGES ARE WITHIN THEIR REGULATION WINDOW – IF ANY VOLTAGE IS INCORRECT POWER OK WILL BE HIGH. THIS OUTPUT IS REFERENCED TO THE COMMON DC OUTPUT RETURN AND CAN BE PULLED UP TO ANY OF THE DC OUTPUT VOLTAGES. SELECT A PULL-UP RESISTOR TO LIMIT THE CURRENT THROUGH THE TRANSISTOR TO LESS THAN 50Ma. (Example – use a 1K pull-up for 5mA of current when pulled up to +5V output.)

NOTES: UNLESS OTHERWISE SPECIFIED
 1. INTERPRET DIMENSIONS AND TOLERANCES PER ANSI Y14.5M-1994.



CAO MAINTAINED. CHANGES SHALL BE INCORPORATED BY THE DESIGN ACTIVITY

REV	DESCRIPTION	DATE	APPROVED
001	INITIAL RELEASE	01/14/09	NWS
002	UPDATED HEATSIK AND PVB	11/09/09	NWS
003	UPDATED TO REV 003 PVB	02/19/10	NWS

UNLESS OTHERWISE SPECIFIED TOLERANCES ARE IN INCHES		APPROVALS	
FINISH	DATE	DATE	TITLE
N/A	08/20/07		VME 450A/VME550A MECH LAYOUT
ORDER	DATE	DATE	AEGIS P/N: VME450A/VME550A-XXX
REV A	08/20/07		REV 003
REV B			REV 002
REV C			REV 001
REV D			REV 000
REV E			REV 000
REV F			REV 000
REV G			REV 000
REV H			REV 000
REV I			REV 000
REV J			REV 000
REV K			REV 000
REV L			REV 000
REV M			REV 000
REV N			REV 000
REV O			REV 000
REV P			REV 000
REV Q			REV 000
REV R			REV 000
REV S			REV 000
REV T			REV 000
REV U			REV 000
REV V			REV 000
REV W			REV 000
REV X			REV 000
REV Y			REV 000
REV Z			REV 000

