

# VPX2703UC500

## Overview

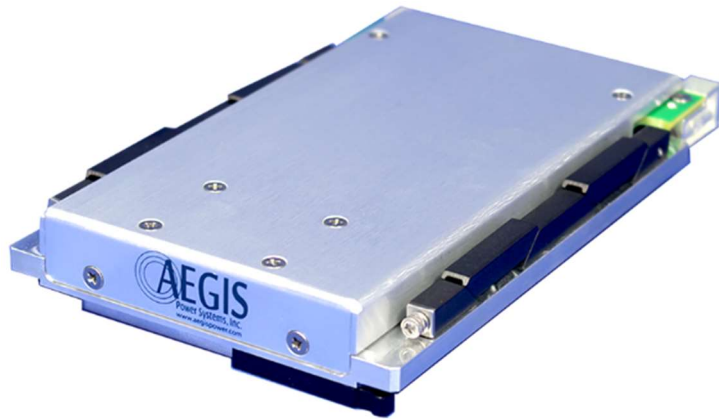
3U VPX DC-DC Power Converter Card  
270Vdc Input, 6 Outputs  
500W Max Combined Output

## Market(s)

MIL-COTS

## Typical Application(s)

Aircraft Electronics Equipment Rack



## Product Highlights

This single slot thin (5HP) filtered 270Vdc VPX2703UC500 power card with six outputs at 500W maximum power, is a COTS military power supply solution designed to meet portions of MIL-STD-810F vibration and shock requirements and designed to meet portions of the MIL-STD-461G EMI requirements. When compared to VPX power supplies using conventional technology, the single-slot VPX2703UC500 provides users with higher efficiency (up to 90%), and higher power (up to 500W). It also has alignment keys that offer keying options when using multiple power supplies in one chassis.

## Features

- 270Vdc per MIL-STD-704F
- 6 Output Voltages, 500W
- Single Slot VPX Power Card
- MIL-STD-461G EMI \*
- VITA 46.11 IPMI I2C interface

\* Designed to meet applicable portions of this standard. Contact Aegis Power Systems, Inc. for specific details.

**Table 1: Maximum Continuous Operating Ratings**

Parameter	Rating	Unit	Notes
Vin max range	200 to 330	Vdc	270V Nominal
Temperature	-40 to +85	°C	Baseplate @ wedgelocks *85C @ 300W total output
Input power	556	W	@ 500W out (270VDC input)
Combined output power	500	W	See Table 2 for DC output variations
Efficiency	90	%	Typical @ nominal conditions

## About Us

Aegis Power Systems, Inc. specializes in the design, development, and manufacture of AC-DC and DC-DC power supplies for high-performance, rugged, critical, and specialty applications. Markets served include defense, industrial, communications, aircraft, shipboard, rack mount, embedded computing, and electric vehicle applications.

[Contact us](#) to find out if this item can be configured or redesigned to meet your specific technology need.

## SPECIFICATIONS

(Typical at 25°C, nominal line and 100% load, unless otherwise specified.)

Parameter	Notes
<b>Input Voltage</b>	Designed to meet MIL-STD-704F Table IV (DC Normal Operation) 250Vdc to 280Vdc, 270 Vdc nominal, continuous operation during Figure 16 (Normal DC voltage transients).
<b>Input Current</b>	2.78A max @ 200Vdc, 2.06A typical @ 270Vdc input (500W out).
<b>Input Power</b>	556W max @ 500W out.
<b>Total Output Power</b>	500W max. all outputs combined.
<b>Output Voltages</b>	(see table 2).
<b>Efficiency</b>	88.5% minimum, 90% typical, 93% max.
<b>Start-Up Time</b>	2 second maximum.
<b>Voltage Set Point</b>	+/- 5% V Out nominal (for any combination).
<b>Line/Load Regulation</b>	+/- 5% V Out nominal (for any combination).
<b>Output Ripple</b>	50mVpk-pk Max. (20 MHz BW) all except; +/-12 Vdc 100mVpk-pk Max.
<b>Current Limit</b>	Short circuit protected with automatic recovery (110% - 130% of rated output).
<b>Temperature</b>	-40°C to +75°C Operating baseplate @ 500W output. (+85°C Operating baseplate @ 300W output) -40°C to +125°C Storage.
<b>Cooling</b>	Conduction cooling through wedgelocks attached to customer rack.
<b>Package</b>	Single slot pluggable slide in rack card.
<b>Dimensions</b>	3U x 5hp x 168.49mm (see mechanical drawing page).
<b>Weight</b>	1.33 lbs. (typical).
<b>Connectors</b>	1ea TE Connectivity 6450849-7 or equivalent (see Table 4).
<b>Vibration</b>	Designed to meet – ANSI/VITA 47-2005 (R2007), Section 4.4.3 Vibration Class V3. MIL-STD-810F, Method 514.5, Procedure I.
<b>Shock:</b>	Designed to meet - ANSI/VITA 47-2005 (R2007), Section 4.5.2 Operating Shock Class OS2 ANSI/VITA 47-2005 (R2007), Section 4.5.3 Bench Handling Shock. MIL-STD-810F, Method 516.5, Procedure I.
<b>Humidity</b>	0 – 95% non-condensing per ANSI/VITA 47-2005 (R2007), Section 4.6 Humidity.
<b>EMI</b>	Designed to meet MIL-STD-461G conducted emissions, when coupled with a front end filter that provides at least 80 dB of attenuation for frequencies greater than 10 kHz.
<b>System Management</b>	VITA 46.11 IPMI I2C interface with monitoring

*Specifications subject to change without notice.*

**Table 2: Voltage Output (Nominal), IMPI Interface, and Temperature Options**

VPX2703UC500-XX	VS1	VS2	VS3	VS4	VS5	VS6
<b>VPX2703UC500-01</b>	+12VDC @ 300W	+3.3VDC @ 100W	+5VDC @ 150W	-12VDC_AUX @ 12W	3.3VDC_AUX @ 3.3W	+12VDC_AUX @ 12W
	No IPMI. -40C to +85C operational and -40C to +125C storage.					
<b>VPX2703UC500-02</b>	Same outputs as -01 version. Includes IPMI interface. -40C to +85C operational and -40C to +125C storage.					

**NOTES:**

1. VS1-VS6 output power levels indicate maximum power available per output. Total combined power of all outputs on VPX2703UC500 cannot exceed 300W @ 85°C
  2. Temperature measured on the unit's baseplate @ wedge locks
  3. VITA 46.11 IPMI I2C interface provides:
    - (VS1-VS6) Output Voltage / (VS1-VS3) Current monitoring
    - On-board temperature monitoring
    - Power good status, PSU serial number & revision
    - Dynamic addressing (GA0 & GA1) allows up to four supplies on same I2C bus
- \* (contact for more details)

**Table 3: ENABLE / INHIBIT**

Control Inputs		Power Outputs	
ENABLE	INHIBIT	3.3V_AUX	PO1, PO2, PO3, +12V_AUX, and -12V_AUX
High	High	Off	Off
High	Low	Off	Off
Low	High	On	On
Low	Low	On	Off

**Table 4: Connector Specifications**

**TE Connectivity Connector 6450849-7 or equivalent**

Contact Designation		Conductor Circuit
P1	40A	-DC_IN/ACN
P2	40A	+DC_IN/ACL
LP1	20A	CHASSIS
A1	<1A	(NC)
B1	<1A	(NC)
C1	<1A	(NC)
D1	<1A	(NC)
A2	<1A	(NC)
B2	<1A	FAIL*
C2	<1A	INHIBIT*
D2	<1A	ENABLE*
A3	<1A	(NC)
B3	<1.5A	+12V_AUX (VS6)
C3	<1A	(NC)
D3	<1A	(NC)
A4	<1.5A	3.3V_AUX (VS5)
B4	<1.5A	3.3V_AUX (VS5)
C4	<1.5A	3.3V_AUX (VS5)
D4	<1.5A	3.3V_AUX (VS5)
A5	<1A	GA0
B5	<1A	GA1
C5	<1A	SM0 (I2C/SCL)
D5	<1A	SM1 (I2C/SDA)
A6	<1A	(NC)
B6	<1A	(NC)
C6	<1.5A	-12V_AUX (VS4)
D6	<1A	SYSRESET*
A7	<1A	(NC)
B7	<1A	(NC)
C7	<1A	(NC)
D7	<1A	SIGNAL_RETURN (Common)
A8	<1A	PO1_SENSE
B8	<1A	PO2_SENSE
C8	<1A	PO3_SENSE
D8	<1A	SENSE_RETURN (Common)
P3	40A	PO3 (VS3)
P4	40A	POWER_RETURN (Common)
P5	40A	POWER_RETURN (Common)
LP2	20A	PO2 (VS2)
P6	40A	PO1 (VS1)

\* Use of a trailing asterisk indicate a logic signal which is active when at the less positive level of its allowable range.

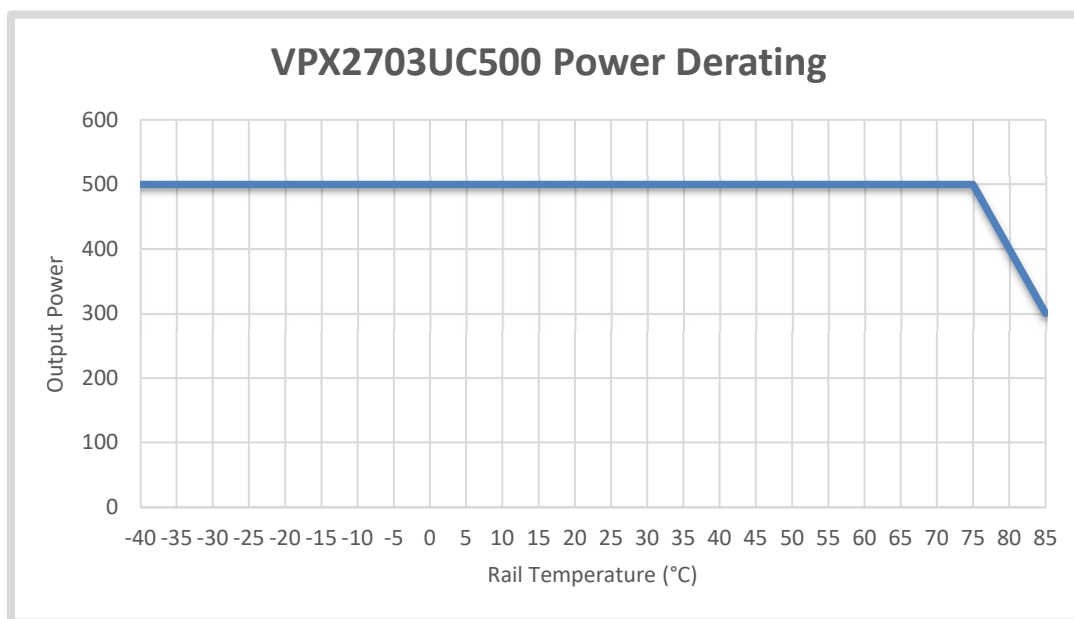
**Table 6: IPMI Commands**

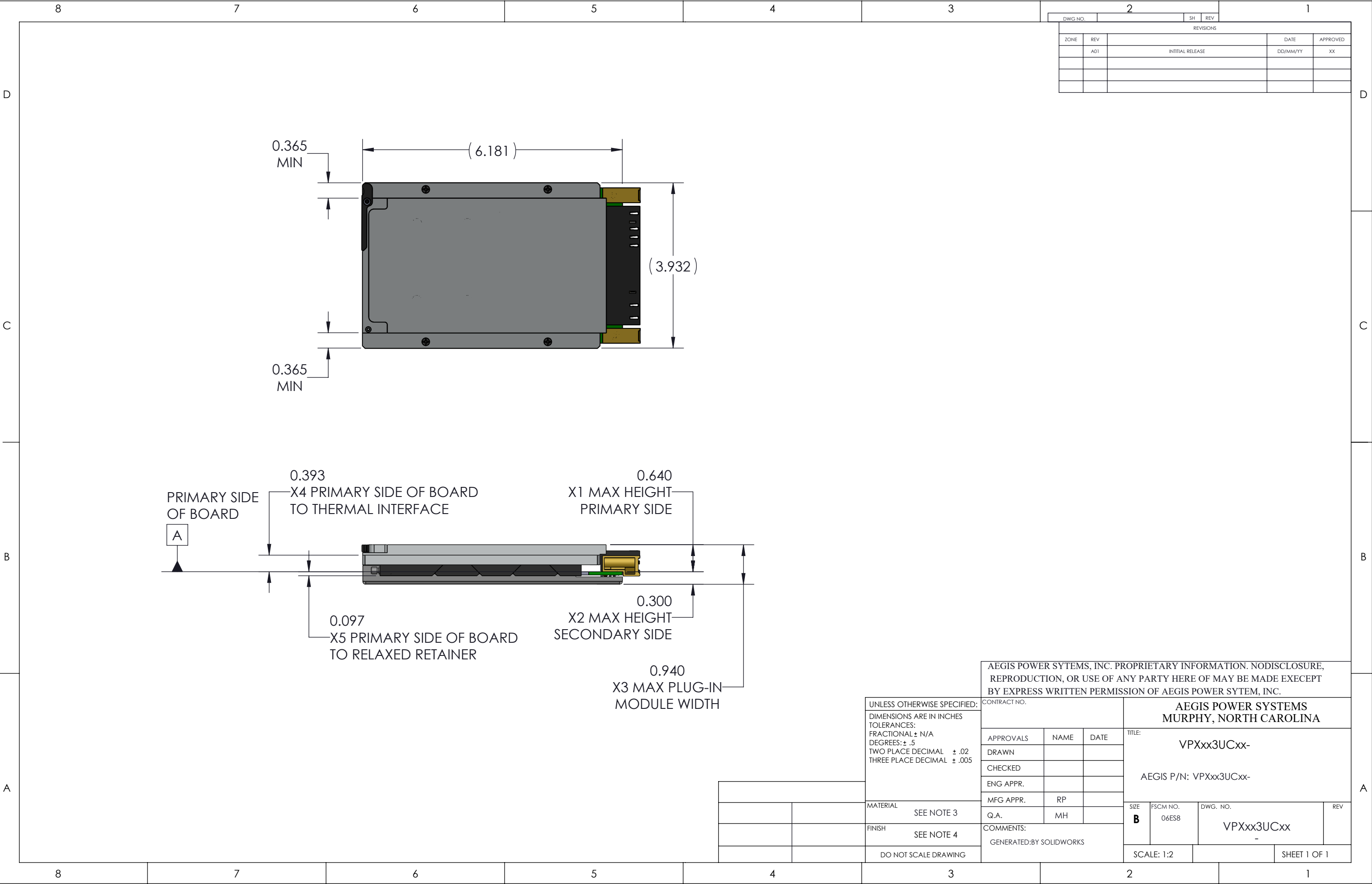
The following three VITA 46.11 IPMI commands are currently implemented in firmware:

- **Get Device ID Command (App (06h) (01h))**  
This command will return the hardware revision, firmware/software revision, command interface revision, system serial number for the power supply.
- **Get Self-Test Results Command (App (06h) (04h))**  
This command will initiate the power supply to verify all available sensor readings are within specified operating ranges. It will report successful if there are no errors, or it will report the sensor that was out of range.
- **Get Sensor Reading Command (S/E (04h) (2Dh))**  
This command will return the data from one of the available sensors. The following sensors are available:

Self-test Sensor Threshold	Sensor Number	Available Data Sensors	Data Reply Format
OT = 90C	00h	BASEPLATE TEMPERATURE	2 BYTE HEX
OV/UV = +/-5%	01h	VS1 OUTPUT VOLTAGE	2 BYTE HEX
OV/UV = +/-5%	05h	+3.3V_AUX OUTPUT VOLTAGE	2 BYTE HEX
OC = 120% Nom.	07h	VS1 OUTPUT CURRENT	2 BYTE HEX
OC = 120% Nom.	08h	+3.3V_AUX OUTPUT CURRENT	2 BYTE HEX
N/A	0Ah	TURN-ON COUNTER	2 BYTE HEX

**Table 7: Power Derating**





2		1	
DWG NO.		SH	REV
REVISIONS			
ZONE	REV	DATE	APPROVED
	A01	INITIAL RELEASE	DD/MM/YY XX

UNLESS OTHERWISE SPECIFIED: DIMENSIONS ARE IN INCHES TOLERANCES: FRACTIONAL ± N/A DEGREES: ± .5 TWO PLACE DECIMAL ± .02 THREE PLACE DECIMAL ± .005				AEGIS POWER SYTEMS, INC. PROPRIETARY INFORMATION. NODISCLOSURE, REPRODUCTION, OR USE OF ANY PARTY HERE OF MAY BE MADE EXECEPT BY EXPRESS WRITTEN PERMISSION OF AEGIS POWER SYTEM, INC.			
MATERIAL		SEE NOTE 3		CONTRACT NO.		AEGIS POWER SYSTEMS MURPHY, NORTH CAROLINA	
FINISH		SEE NOTE 4		APPROVALS	NAME	DATE	TITLE:
DO NOT SCALE DRAWING				DRAWN			VPXxx3UCxx-
				CHECKED			AEGIS P/N: VPXxx3UCxx-
				ENG APPR.			
				MFG APPR.	RP		
				Q.A.	MH		
				COMMENTS: GENERATED:BY SOLIDWORKS		SIZE	FSCM NO.
						B	06ES8
						DWG. NO.	REV
						VPXxx3UCxx	
						SCALE: 1:2	SHEET 1 OF 1